

**Amendments to the Claims:**

1. (currently amended) A storage virtualization computer system comprising:

5 a host entity for issuing IO requests;  
an external storage virtualization controller coupled to said host entity for  
executing IO operations in response to said IO requests; and  
at least one physical storage device (PSD), each coupled to the storage  
virtualization controller through a point-to-point serial-signal interconnect,  
for providing storage to the storage virtualization computer system through  
the storage virtualization controller;

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wherein said storage virtualization controller comprises:

a central processing circuitry for performing said IO operations in response to  
said IO requests of said host entity;  
15 at least one IO device interconnect controller coupled to said central processing  
circuitry;  
at least one host-side IO device interconnect port provided in [[a]] one of said at  
least one IO device interconnect controller for coupling to said host entity;  
and

20 at least one device-side IO device interconnect port provided in [[a]] one of said at  
least one IO device interconnect controller for coupling to [[a]] one of said at least  
one physical storage device through said point-to-point serial-signal interconnect,  
said device-side IO device interconnect port being a serial port for point-to-point  
serial-signal transmission;

25 wherein said computer system further comprises a detachable canister attached to said  
storage virtualization controller for containing one of said at least one PSD  
therein;

wherein said storage virtualization controller is configured to define at least one  
logical media unit consisting of sections of at least one said PSD; and

30 wherein said SVC issues a device-side IO request to said IO device interconnect

controller, and said IO device interconnect controller re-formats said device-side IO request and accompanying IO data into at least one data packet for transmission to said PSD through said device-side IO device interconnect port.

5     2.     (original) The storage virtualization computer system of claim 1 wherein said point-to-point serial-signal interconnect is a Serial ATA IO device interconnect.

3.     (previously presented) The computer system of one of claims 1 and 2, wherein said at least one PSD comprises a SATA PSD.

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4.     (currently amended)     The computer system of one of claims 1 and 2, wherein said at least one PSD comprises a PATA PSD and a serial-to-parallel converter is provided between [[a]] one of said device-side IO device interconnect port and said PATA PSD.

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5.     (cancelled)

6.     (previously presented) The computer system of one of claims 1 and 2, wherein said at least one PSD can be detached from said storage virtualization controller when said storage virtualization controller is on-line.

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7.     (previously presented) The computer system of one of claims 1 and 2, wherein said at least one PSD can be attached to said storage virtualization controller when said storage virtualization controller is on-line.

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8.     (cancelled)

9.     (cancelled)

30     10.   (currently amended)     The storage virtualization computer system of

claim 1 wherein [[a]] one of said host-side IO device interconnect port and [[a]] one of said device-side IO device interconnect port are provided in a same IO device interconnect controller.

5        11. (currently amended) The storage virtualization computer system of claim 1 wherein said at least one IO device interconnect controller comprises a plurality of IO device interconnect controllers; wherein [[a]] one of said host-side IO device interconnect port and [[a]] one of said device-side IO device interconnect port are provided in different IO device interconnect controllers.

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12. (original) The computer system of one of claims 1 and 2, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device interconnect.

15        13. (original) The computer system of claim 12, wherein said storage virtualization controller is configured to present redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.

20        14. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

25        15. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

30        16. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.

17. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

5 18. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

10 19. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI (SAS) operating in target mode.

20. (previously presented) The computer system of claim 1, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

15 21. (currently amended) A storage virtualization subsystem for providing storage to a host entity, comprising:

an external storage virtualization controller for connecting to the host entity and executing IO operations in response to IO requests issued from said host entity; and

20 at least one physical storage device (PSD), each coupled to the storage virtualization controller through a point-to-point serial-signal interconnect, for providing storage to the host entity through the storage virtualization controller;

25 wherein said storage virtualization controller comprises:

a central processing circuitry for performing said IO operations in response to said IO requests of said host entity;

30 at least one IO device interconnect controller coupled to said central processing

circuitry;

at least one host-side IO device interconnect port provided in [[a]] one of said at least one IO device interconnect controller for coupling to said host entity; and

5 at least one device-side IO device interconnect port provided in [[a]] one of said at least one IO device interconnect controller for coupling to [[a]] one of said at least one physical storage device through said point-to-point serial-signal interconnect, said device-side IO device interconnect port being a serial port for point-to-point serial-signal transmission;

10 wherein said subsystem further comprises a detachable canister attached to said storage virtualization controller for containing one of said at least one PSD therein;

wherein said storage virtualization controller is configured to define at least one logical media unit consisting of sections of at least one said PSD; and

15 wherein said SVC issues a device-side IO request to said IO device interconnect controller, and said IO device interconnect controller re-formats said device-side IO request and accompanying IO data into at least one data packet for transmission to said PSD through said device-side IO device interconnect port.

20 22. (original) The storage virtualization subsystem of claim 21 wherein said point-to-point serial-signal interconnect is a Serial ATA IO device interconnect.

23. (cancelled)

25 24. (currently amended) The storage virtualization subsystem of claim 21 wherein [[a]] one of said host-side IO device interconnect port and [[a]] one of said device-side IO device interconnect port are provided in a same IO device interconnect controller.

30 25. (currently amended) The storage virtualization subsystem of claim



21 wherein said at least one IO device interconnect controller comprises a plurality of  
IO device interconnect controllers; wherein [[a]] one of said host-side IO device  
interconnect port and [[a]] one of said device-side IO device interconnect port are  
provided in different IO device interconnect controllers.

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26. (previously presented) The storage virtualization subsystem of claim 21,  
wherein said at least one PSD comprises a SATA PSD.

27. (previously presented) The storage virtualization subsystem of claim 21,  
10 wherein said storage virtualization controller comprises a plurality of host-side IO  
device interconnect ports each for coupling to a host-side IO device interconnect.

28. (cancelled)

15 29. (original) The storage virtualization subsystem of claim 27, wherein said  
storage virtualization controller is configured to present redundantly a logical media  
unit on at least two of said plurality of host-side IO device interconnect ports.

20 30. (currently amended) The storage virtualization subsystem of claim 21,  
wherein said at least one PSD comprises a PATA PSD and a serial-to-parallel  
converter is provided between [[a]] one of said device-side IO device interconnect  
port and said PATA PSD.

31. (cancelled)

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32. (previously presented) The storage virtualization subsystem of claim 21,  
wherein said at least one PSD can be detached from said storage virtualization  
controller when said storage virtualization controller is on-line.

30 33. (previously presented) The storage virtualization subsystem of claim 21,

wherein said at least one PSD can be attached to said storage virtualization controller when said storage virtualization controller is on-line.

34. (previously presented) The storage virtualization subsystem of claim 21,  
5 wherein said storage virtualization controller further comprises at least one multiple-device device-side expansion port for accommodating an additional set of at least one PSD.

35. (previously presented) The storage virtualization subsystem of claim 21,  
10 wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.

36. (previously presented) The storage virtualization subsystem of claim 21,  
15 wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

37. (previously presented) The storage virtualization subsystem of claim 21,  
wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.  
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38. (previously presented) The storage virtualization subsystem of claim 21,  
wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

39. (previously presented) The storage virtualization subsystem of claim 21,  
25 wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

40. (previously presented) The storage virtualization subsystem of claim 21,  
30 wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI

(SAS) operating in target mode.

41. (previously presented) The storage virtualization subsystem of claim 21,  
wherein at least one said host-side IO device interconnect port is Serial ATA  
5 operating in target mode.

42. (previously presented) The storage virtualization subsystem of claim 21  
further comprising an enclosure management services mechanism.

10 43. (original) The storage virtualization subsystem of claim 42, wherein said  
enclosure management services mechanism manages and monitors at least one of the  
following devices belonging to the storage virtualization subsystem: power supplies,  
fans, temperature sensors, voltages, uninterruptible power supplies, batteries, LEDs,  
audible alarms, PSD canister locks, door locks.

15 44. (original) The storage virtualization subsystem of claim 42, wherein said  
enclosure management services mechanism is configured to support direct-connect  
EMS configuration.

20 45. (original) The storage virtualization subsystem of claim 42, wherein said  
enclosure management services mechanism is configured to support device-forwarded  
EMS configuration.

25 46. (original) The storage virtualization subsystem of claim 42, wherein said  
enclosure management services mechanism is configured to support direct-connect  
EMS configuration and device-forwarded EMS configuration.

30 47. (original) The storage virtualization subsystem of claim 42, wherein said  
enclosure management services mechanism is configured to support SES enclosure  
management services protocol.



48. (original) The storage virtualization subsystem of claim 42, wherein said enclosure management services mechanism is configured to support SAF-TE enclosure management services protocol.

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49. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises I2C latches to communicate with said storage virtualization controller.

10 50. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises status-monitoring circuitry to communicate with said storage virtualization controller.

15 51. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises the following mechanism to communicate with said storage virtualization controller: I2C latches and status-monitoring circuitry.

52. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises a CPU for running a program.

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53. (original) The storage virtualization subsystem of claim 42, wherein said EMS mechanism further comprises at least one I2C interconnect as a primary communication media to said storage virtualization controller.

25 54. (withdrawn) An external storage virtualization controller for executing IO operations in response to IO requests from a host entity, comprising:

a central processing circuitry for performing IO operations in response to said IO requests of said host entity;

at least one IO device interconnect controller coupled to said central processing

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circuitry;

at least one host-side IO device interconnect port provided in a said at least one  
IO device interconnect controller for coupling to said host entity; and  
at least one device-side IO device interconnect port provided in a said at least  
one IO device interconnect controller for coupling to and performing  
5 point-to-point serial-signal transmission with at least one physical storage  
device.

55. (withdrawn) The external storage virtualization controller of claim 54  
wherein said device-side IO device interconnect controller comprises at least one  
10 Serial ATA port, each for connecting to a said at least one physical storage device  
through a Serial ATA IO device interconnect.

56. (withdrawn) The external storage virtualization controller of one of claims  
54 and 55 wherein a said host-side IO device interconnect port and a said device-side  
15 IO device interconnect port are provided in the same IO device interconnect  
controller.

57. (withdrawn) The external storage virtualization controller of one of claims  
54 and 55 wherein a said host-side IO device interconnect port and a said device-side  
20 IO device interconnect port are provided in different IO device interconnect  
controllers.

58. (withdrawn) The external storage virtualization controller of one of claims  
54 and 55 wherein said device-side IO device interconnect controller further  
25 comprises a PCI/PCI-X interface for connecting to the central processing circuit.

59. (withdrawn) The external storage virtualization controller of one of claims  
54 and 55 wherein said device-side IO device interconnect controller further  
comprises a PCI Express interface for connecting to the central processing circuit.

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60. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein said storage virtualization controller comprises a plurality of host-side IO device interconnect ports each for coupling to a host-side IO device  
5 interconnect.

61. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein said storage virtualization controller is configured for defining at least one logical media unit consisting of sections of said at least one PSD.  
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62. (withdrawn) The external storage virtualization controller of claim 60, wherein said storage virtualization controller is configured for presenting redundantly a logical media unit on at least two of said plurality of host-side IO device interconnect ports.  
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63. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said PSD is a direct-access storage device (DASD) and said storage virtualization controller is configured for defining a logical media unit consisting of sections of at least one said direct access storage device and said logical  
20 media unit is of RAID level or a combination of RAID levels, whereby said logical media unit is contiguously addressable by said host entity.

64. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 further comprising at least one multiple-device device-side expansion port  
25 for accommodating a second set of at least one PSD.

65. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting point-to-point connectivity in target mode.  
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66. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting private loop connectivity in target mode.

5        67. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Fibre Channel supporting public loop connectivity in target mode.

10       68. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is parallel SCSI operating in target mode.

15       69. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is ethernet supporting the iSCSI protocol operating in target mode.

20       70. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Serial-Attached SCSI (SAS) operating in target mode.

      71. (withdrawn) The external storage virtualization controller of one of claims 54 and 55, wherein at least one said host-side IO device interconnect port is Serial ATA operating in target mode.

25       72. (withdrawn) The external storage virtualization controller of one of claims 54 and 55 further comprising an enclosure management services mechanism.

30       73. (withdrawn) The storage virtualization controller of claim 72, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration.

74. (withdrawn) The storage virtualization controller of claim 72, wherein said enclosure management services mechanism is configured to support device-forwarded EMS configuration.

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75. (withdrawn) The storage virtualization controller of claim 72, wherein said enclosure management services mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

10 76. (withdrawn) The storage virtualization controller of claim 72, wherein said storage virtualization controller is configured to support SES enclosure management services protocol.

15 77. (withdrawn) The storage virtualization controller of claim 72, wherein said storage virtualization controller is configured to support SAF-TE enclosure management services protocol.

20 78. (currently amended) A method for performing storage virtualization in a computer system with an external storage virtualization controller of the computer system, the method comprising:

receiving an IO request from a host entity of the computer system with the storage virtualization controller;  
parsing the IO request with the storage virtualization controller to decide at least one IO operation to perform in response to said IO request;  
25 performing at least one IO operation with the storage virtualization controller to access at least one physical storage device (PSD) of the computer system in point-to-point serial-signal transmission; and  
performing said at least one IO operation by a central processing circuitry in the storage virtualization controller in response to said IO request of said host  
30 entity;



wherein said storage virtualization controller includes:

5           at least one IO device interconnect controller coupled to said central processing circuitry;

          at least one host-side IO device interconnect port provided in [[a]] one of said at least one IO device interconnect controller for coupling to said host entity; and

10           at least one device-side IO device interconnect port provided in [[a]] one of said at least one IO device interconnect controller for coupling to [[a]] one of said at least one physical storage device through said point-to-point serial-signal interconnect, said device-side IO device interconnect port being a serial port for point-to-point serial-signal transmission;

15           wherein said computer system further comprises a detachable canister attached to said storage virtualization controller for containing one of said at least one PSD therein;

wherein said storage virtualization controller is configured to define at least one logical media unit consisting of sections of at least one said PSD; and

20           wherein said SVC issues a device-side IO request to said IO device interconnect controller, and said IO device interconnect controller re-formats said device-side IO request and accompanying IO data into at least one data packet for transmission to said PSD through said device-side IO device interconnect port.

25           79. (original) The method of claim 78 wherein the point-to-point serial-signal transmission is performed in a format complying with a Serial ATA protocol.

          80. (original) The method of one of claims 78 and 79 further comprising the step of providing an enclosure management services mechanism.

30           81. (original) The method of claim 80 further comprising the step of executing

said enclosure management services mechanism when said mechanism is configured to support direct-connect EMS configuration.

5 82. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said mechanism is configured to support device-forwarded EMS configuration.

10 83. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said mechanism is configured to support direct-connect EMS configuration and device-forwarded EMS configuration.

15 84. (original) The method of claim 80 further comprising the step of executing said enclosure management services mechanism when said storage virtualization controller is configured to support SES enclosure management services protocol.

20 85. (original) The method of claim 80, further comprising the step of executing said enclosure management services mechanism when said storage virtualization controller is configured to support SAF-TE enclosure management services protocol.

86. (previously presented) The method of one of claims 78 and 79, wherein said at least one PSD comprises a SATA PSD.

25 87. (previously presented) The method of one of claims 78 and 79, wherein at least one PSD comprises a PATA PSD and serial signals in said serial signal transmission are converted by a serial-to-parallel converter to parallel signals compliant with said PATA PSD.

30 88. (cancelled)

89. (currently amended) The method of one of claims 78 and 79 ~~claim 88~~,  
wherein said data packet comprises a start segment at the beginning indicating the  
start of said data packet, an end segment at the end indicating the end of the data  
packet, a payload data segment containing actual IO information to transmit through  
5 the device-side IO device interconnect, and a check data segment containing check  
codes derived from said payload data for checking the correctness of said payload data  
after transmission.

90. (currently amended) A computer-readable storage medium having a  
10 computer program code stored therein that is capable of causing a computer system  
having an external storage virtualization controller and at least one physical storage  
device connected to the storage virtualization controller to perform the steps of:

receiving an IO request from a host entity of the computer system with the  
storage virtualization controller;  
15 parsing the IO request with the storage virtualization controller to decide at least  
one IO operation to perform in response to said IO request;  
performing said at least one IO operation with the storage virtualization  
controller to access said at least one physical storage device (PSD) in  
point-to-point serial-signal transmission; and  
20 performing said at least one IO operation by a central processing circuitry in the  
storage virtualization controller in response to said IO request of said host  
entity;

wherein said storage virtualization controller includes:

25 at least one IO device interconnect controller coupled to said central processing  
circuitry;  
at least one host-side IO device interconnect port provided in [[a]] one of said at  
least one IO device interconnect controller for coupling to said host entity;  
30 and

at least one device-side IO device interconnect port provided in [[a]] one of said  
at least one IO device interconnect controller for coupling to [[a]] one of  
said at least one physical storage device through said point-to-point  
serial-signal interconnect, said device-side IO device interconnect port  
5 being a serial port for point-to-point serial-signal transmission;  
wherein said computer system further comprises a detachable canister attached to said  
storage virtualization controller for containing one of said at least one PSD  
therein;  
wherein said storage virtualization controller is configured to define at least one  
10 logical media unit consisting of sections of at least one said PSD; and  
wherein said SVC issues a device-side IO request to said IO device interconnect  
controller, and said IO device interconnect controller re-formats said device-side  
IO request and accompanying IO data into at least one data packet for  
transmission to said PSD through said device-side IO device interconnect port.

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91. (original) The computer-readable storage medium of claim 90 wherein the point-to-point serial-signal transmission is performed in a format complying with a Serial ATA protocol.

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92. (previously presented) The computer-readable storage medium of one of claims 90 and 91, wherein said at least one PSD comprises a SATA PSD.

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93. (previously presented) The computer-readable storage medium of one of claims 90 and 91, wherein said at least one PSD comprises a PATA PSD and serial signals in said serial signal transmission are converted by a serial-to-parallel converter to parallel signals compliant with said PATA PSD.

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94. (original) The computer-readable storage medium of one of claims 90 and 91, wherein the step of performing said at least one IO operation comprises issuing a device-side IO request to said device-side IO device interconnect controller and

re-formatting said device-side IO request and accompanying IO data into at least one data packet for transmission.

5        95. (original) The computer-readable storage medium of claim 94, wherein said data packet comprises a start segment at the beginning indicating the start of said data packet, an end segment at the end indicating the end of the data packet, a payload data segment containing actual IO information to transmit through the device-side IO device interconnect, and a check data segment containing check codes derived from said payload data for checking the correctness of said payload data after transmission.

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96. (cancelled)